

**MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY
(AUTONOMOUS)**

M. Tech – I Year – I Sem. (VLSI & Embedded Systems)

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
M.TECH – VLSI & EMBEDDED SYSTEMS
COURSE STRUCTURE**

I Year I Semester

S.NO.	SUBJECT CODE	SUBJECT	L	T/ P/ D	C	MAX MARKS	
						INT	EXT
1	R17D6801	VLSI Technology & Design	3	-	3	30	70
2	R17D6802	CPLD & FPGA Architectures& Applications	3	-	3	30	70
3	R17D6803	Embedded System design	3	-	3	30	70
4	R17D6804	ELECTIVE-I 1.Digital System Design	3	-	3	30	70
	R17D6805	2.CMOS Analog Integrated Circuit Design					
	R17D6806	3.Hardware Software Co-design					
5	R17D6807	ELECTIVE-II 1.CMOS Digital Integrated Circuit design	3	-	3	30	70
	R17D6808	2.Algorithms for VLSI Design Automation					
	R17D6809	3.Advanced Digital Signal Processing					
6	R17DEC51	OPEN ELECTIVE –I 1.Embedded Systems Programming	3	-	3	30	70
	R17DCS51	2.Scripting Languages					
	R17DME51	3.Non-conventional Energy Sources					
	R17DAE51	4.Mathematical Modeling Techniques					
7	R17D6881	VLSI Laboratory	-	3	2	30	70
8	R17D6882	Technical Seminar-I	-	-	2	50	-
Total			18	3	22	260	490

I Year II Semester

S.NO.	SUBJECT	SUBJECT	L	T/P/D	C	MAX MARKS
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	CODE					INT	EXT
1	R17D6810	Embedded Real Time Operating Systems	3	-	3	30	70
2	R17D6811	CMOS Mixed Signal Circuit Design	3	-	3	30	70
3	R17D6812	Low Power VLSI Design	3	-	3	30	70
4	R17D6813 R17D6814 R17D6815	ELECTIVE – III 1.Adhoc –Wireless Networks 2.Digital Signal Processors & Architectures 3.Embedded Networking	3	-	3	30	70
5	R17D6816 R17D6817 R17D6818	ELECTIVE- IV 1.System On Chip Architecture 2.Design For Testability 3.Multimedia Signal Coding	3	-	3	30	70
6	R17DEC52 R17DCS52 R17DME52 R17DCS53	OPEN ELECTIVE- II 1.Internet of Things 2.Information Security 3.Industrial Management 4.Research Methodology	3	-	3	30	70
7	R17D6883	Embedded Systems Laboratory	-	3	2	30	70
8	R17D6884	Technical Seminar-II	-	-	2	50	-
Total			18	3	22	260	490

II Year I Semester

S.NO.	SUBJECT CODE	SUBJECT	L	T/P/D	C	MAX MARKS	
						INT	EXT
1	R17D6885	Technical Seminar-III	-	-	2	50	-
2	R17D6891	Project Review-I	-	-	10	100	-
3	R17D6892	Project Review-II	-	-	10	100	-
Total			-	-	22	-	-

II Year II Semester

S.NO.	SUBJECT CODE	SUBJECT	L	T/P/D	C	MAX MARKS	
						INT	EXT
1	R17D6886	Technical Seminar-IV	-	-	2	50	-
	R17D6893	Project Review-III	-	-	10	100	-
2	R17D6894	Project Viva-voce	-	-	10	-	100
Total			-	-	22	-	-