

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

AN AUTONOMOUS INSTITUTE- UGC, GOVT. OF. INDIA

M.TECH - I SEMESTER– R-17- REGULAR EXTERNAL EXAMINATIONS, JAN/FEB-2018

T I M E T A B L E

TIMINGS: FN: 10.00 AM TO 01.00 PM

	30-01-2018 FN	01-02-2018 FN	03-02-2018 FN	06-02-2018 FN	08-02-2018 FN	10-02-2018 FN
	TUESDAY	THURSDAY	SATURDAY	TUESDAY	THURSDAY	SATURDAY
MACHINE DESIGN (15-MD)	Advanced Mechanical Engineering Design R17D1501	Advanced Mechanics of Solids R17D1502	Fatigue, Creep & Fracture Mechanics R17D1503	Advanced Finite Element Analysis R17D1504	Advanced Mechanics of Composite Materials R17D1507	Embedded Systems Programming R17DEC51
				Gear Engineering R17D1505	Advanced Computer Aided Design R17D1508	Scripting Languages R17DCS51
				Theory of Elasticity & Plasticity R17D1506	Applied Tribology R17D1509	Non Conventional Energy Sources R17DME51
THERMAL ENGINEERING (21-TE)	Advanced Thermodynamics R17D2101	Advanced Heat and Mass Transfer R17D2102	Advanced Fluid Mechanics R17D2103	Cryogenics Engineering R17D2104	Advanced I.C. Engines R17D2107	Embedded Systems Programming R17DEC51
				Solar Energy Technology R17D1505	Design of Heat Exchangers R17D2108	Scripting Languages R17DCS51
				Turbomachines R17D1506	Energy and Environmental Engineering R17D2109	Non Conventional Energy Sources R17DME51
						Mathematical Modeling Techniques R17DAE51

COMPUTER SCIENCE & ENGINEERING (58-CSE)	Data Structures and Algorithms R17D5801	Advanced Operating Systems R17D5802	Computer System Design R17D5803	Software Process and Project Management R17D5804	Machine Learning R17D5807	Embedded Systems Programming R17DEC51
				Cloud Computing R17D5813	Software Architecture and Design Patterns R17D5808	Scripting Languages R17DCS51
				Android Application Development R17D5806	Parallel and Distributed Algorithms R17D5809	Non Conventional Energy Sources R17DME51
						Mathematical Modeling Techniques R17DAE51
VLSI & Embedded Systems (68-VLSI&ES)	VLSI Technology & Design (R17D6801)	CPLD & FPGA Architectures Applications (R17D6802)	Embedded System design (R17D9303)	Digital System Design R17D9309	CMOS Digital Integrated Circuitdesign R15D6807	Embedded Systems Programming R17DEC51
				CMOS Analog Integrated Circuit Design R17D6805	Algorithms for VLSI Design Automation R17D6808	Scripting Languages R17DCS51
				Hardware Software Co-design R17D6806	Advanced Digital Signal Processing R17D6809	Non Conventional Energy Sources R17DME51
						Mathematical Modeling Techniques R17DAE51

AEROSPACE ENGINEERING (76-ASP)	Aerodynamics of Flight Vehicles R17D7601	Engineering Analysis Of Flight Vehicles R17D7602	Rocket And Missile Technology R17D7603	Fundamentals Of Aerospace Engineering R17D7604	Modeling And Simulation Of Fluid Flows R15D7607	Embedded Systems Programming R17DEC51
				Air-breathing Propulsion And Design R17D7605	Computational Structural Analysis R15D7608	Scripting Languages R17DCS51
				Flight Vehicle Structures R17D7606	Integrated Aircraft systems R15D7609	Non Conventional Energy Sources R17DME51
						Mathematical Modeling Techniques R17DAE51
SYSTEMS & SIGNAL PROCESSING (93-SSP)	Transform Techniques R17D9301	Multimedia Processing R17D9302	Embedded System Design R17D9303	Advanced Data Communications R17D9307	Biomedical Signal Processing R17D9304	Embedded Systems Programming R17DEC51
				Detection and Estimation Theory R17D9308	Radar Signal Processing R17D9305	Scripting Languages R17DCS51
				Digital System Design R17D9309	VLSI Signal Processing R17D9306	Non Conventional Energy Sources R17DME51
						Mathematical Modeling Techniques R17DAE51

(i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.

(ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

DATE: 04-01-2018

Sd/-

CONTROLLER OF EXAMINATION

Sd/-

CHIEF CONTROLLER OF EXAMINATION