

Code No: **R20A0511****MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY**

(Autonomous Institution – UGC, Govt. of India)

III B.Tech I Semester Supplementary Examinations, June/July- 2024**Software Engineering****(CSE, IT, CSE-CS, CSE-AIML, CSE-IOT & B.Tech-AIML)**

Roll No									

Time: 3 hours**Max. Marks: 70**

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

1 **A** What is the difference between Incremental process models, and Evolutionary process models. [7M]

B Explain about process frame work with a neat sketch. [7M]

OR

2 **A** What is the Capability Maturity Model Integration (CMMI)? [7M]

B Discuss about the Unified process and explain it. [7M]

SECTION-II

3 **A** Explain about context models, behavioural models and Data models. [7M]

B What is the importance of UML Diagrams with example [7M]

OR

4 **A** Describe about requirements elicitation and analysis in software engineering? [7M]

B Compare functional and non-functional requirements [7M]

SECTION-III

5 **A** Discuss about architectural styles, patterns and architectural design. [7M]

B Illustrate about user interface analysis and design. [7M]

OR

6 **A** Explain about interface design steps and design evaluation. [7M]

B Analyse about software architecture and data design. [7M]

SECTION-IV

7 **A** Explain about the strategic approach to software testing. [7M]

B Compare Black-Box and White-Box testing with example. [7M]

OR

8 **A** Describe about Risk refinement RMMM and RMMM Plan. [7M]

B Discuss about Validation testing and System testing, [7M]

SECTION-V

9 **A** Write about Statistical Software quality Assurance with example. [7M]

B Briefly discuss about the Case Study of ATM Management System [7M]

OR

10 **A** Describe about the ISO 9000 quality standards. [7M]

B Describe about software quality assurance and software reviews. [7M]

Code No: **R20A0512****MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY**

(Autonomous Institution – UGC, Govt. of India)

III B.Tech I Semester Supplementary Examinations, June/July 2024**Compiler Design
(CSE & CSE-AIML)**

Roll No									
----------------	--	--	--	--	--	--	--	--	--

Time: 3 hours**Max. Marks: 70**

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

- 1 Recognise the functions of a Lexical Analyzer. State the reasons for the Separation of Analyses programs into Lexical, Syntax, and Semantic Analyses. [14M]

OR

- 2 A Classify the parser into various types. [7M]
B How do you organize NFA from Regular expression? [7M]

SECTION-II

- 3 A Discover the rule to eliminate left recursion in a grammar. Prepare and eliminate the left recursion for the grammar. [7M]

$$S \rightarrow Aa \mid b$$

$$A \rightarrow Ac \mid Sd \mid \epsilon$$

- B Identify the advantages and disadvantages of LR Parser. [7M]

OR

- 4 A Construct the predictive parsing table for the following grammar and verify the string (a, a) is accepting or not. [9M]

$$S \rightarrow (L) \mid a$$

$$L \rightarrow L, S \mid S$$

- B Check whether the following grammar is a LL(1) grammar [5M]

$$S \rightarrow iEtS \mid iEtSeS \mid a$$

$$E \rightarrow b$$

Also define the FIRST and FOLLOW procedures.

SECTION-III

- 5 A Write syntax directed definition for simple desk calculator. Using this definition draw annotated parse tree for $3*5+4n$. [7M]

- B Explain the unification algorithm by us type checking concepts [7M]

OR

- 6 A Explain the use of symbol table in compilation process. List out the various attributes for implementing the symbol table [7M]

- B Generate code for the following: i) $x=f(a)+f(a)+f(a)$ ii) $x=f(f(a))$ [7M]

SECTION-IV

- 7 Discuss about the followings: [14M]

- Copy propagation
- Dead code Elimination
- Code motion.

OR

- 8 A Consider the following loop, generate three address code and draw the flow graph [7M]

```
Begin
  Prod=0
  i=1
  do
    Begin
      Prod=Prod+a[i]*b[i]
      i=i+1
    End
  While (i≤20)
End
```

- B Relate the static and dynamic storage allocation for any program segment [7M]

SECTION-V

- 9 A Explain in detail about global common sub expression elimination technique. [7M]

- B Define dataflow analysis? List out the procedures to analyse the data flow of structured programs? [7M]

OR

- 10 Write the next-use information for each line of the following 3 address code basic block. [14M]

```
a:=b+c x:=a+b b:=a-d c:=b+c d=a-d y=a-d
***
```


handling text data effectively.

- B** Discuss the concept of lists in PERL. Provide examples of list operations and explain how lists differ from arrays. Discuss situations where using lists is advantageous. [7M]

SECTION-IV

- 7** **A** Differentiate between indexed and associative arrays in PHP. Provide examples of each type and explain when to use one over the other. [7M]

- B** Explain the concept of scalars in PHP. Provide examples of scalar variables and discuss the importance of variable naming conventions [7M]

OR

- 8** **A** Enumerate and explain different types of operators in PHP (arithmetic, relational, logical). Provide examples of each type and discuss their applications in programming [7M]

- B** Discuss the various conditional statements in PHP (if, else if, else). Provide examples of using these statements and explain how they contribute to controlling the flow of a program. [7M]

SECTION-V

- 9** **A** Explain the purpose of modules in Ruby. Provide examples of creating and using modules and discuss how they facilitate code organization and reuse. [7M]

- B** Discuss the concept of arrays in Ruby. Provide examples of array operations and discuss their importance in managing collections of data [7M]

OR

- 10** **A** Discuss the role of blocks in Ruby. Provide examples of using blocks and explain how they enhance code readability and maintainability. [7M]

- B** Explain the concept of methods in Ruby. Provide examples of creating methods with and without parameters and discuss the advantages of using methods in programming. [7M]

Code No: **R20A6902****MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY**
(Autonomous Institution – UGC, Govt. of India)**III B.Tech I Semester Supplementary Examinations, June/July 2024****Embedded Systems****(CSE)**

Roll No									
----------------	--	--	--	--	--	--	--	--	--

Time: 3 hours**Max. Marks: 70****Note:** This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

- 1 **A** Describe the key features and functionalities of the 8051 microcontroller. [7M]
 B Explain the architecture of the 8051 microcontroller, highlighting its major components and their interconnections. [7M]

OR

- 2 **A** Illustrate the pin diagram of the 8086 microcontroller. Explain the functions of different pins and their roles in facilitating communication with external devices or components. [7M]
 B Evaluate the instruction set architecture (ISA) of the 8086 microcontroller. Discuss the types of instructions available and their significance in executing various operations within embedded systems. [7M]

SECTION-II

- 3 **A** Describe the fundamental characteristics of embedded systems. How do these systems differ from general-purpose computing systems in terms of size, complexity, and functionality? [7M]
 B Explain the significance of real-time operation as a characteristic of embedded systems. [7M]

OR

- 4 **A** Discuss the resource constraints commonly encountered in embedded systems. [7M]
 B Identify and explain key quality attributes (e.g., performance, usability, maintainability) relevant to embedded systems. [7M]

SECTION-III

- 5 **A** Assess the challenges associated with integrating multiple communication interfaces into an embedded system. How do designers ensure compatibility and interoperability between different interfaces? [7M]
 B Evaluate the importance of security and reliability in communication interfaces of embedded systems. Discuss the measures taken to ensure data integrity, confidentiality, and robustness [7M]

OR

- 6 **A** Analyze the evolving landscape of communication interfaces in embedded systems. Explain about SPI interface. [7M]
 B Explain the role of GPRS in embedded systems. How does GPRS enable connectivity and data transmission in remote embedded devices? [7M]

SECTION-IV

7 **A** Analyze the role and relevance of assembly language in embedded firmware development. **[7M]**

B Discuss scenarios where using assembly language is crucial and where it might be less advantageous compared to high-level languages. **[7M]**

OR

8 **A** Evaluate the benefits of high-level language-based development in embedded firmware design. **[7M]**

B Predict the future trends in embedded firmware design approaches and development languages. Discuss potential advancements and adaptations that might influence the choice of design methodologies and programming languages in the coming years. **[7M]**

SECTION-V

9 **A** Explain the process of interfacing LED, LCD, switches, and sensors using C programming. Discuss the specific challenges and considerations when implementing these interfaces in embedded systems, highlighting best practices. **[7M]**

B Assess the optimization strategies available in C programming for efficient interfacing with 8051 microcontrollers. Discuss how to optimize code for better resource utilization and real-time performance when interfacing with different components. **[7M]**

OR

10 **A** Evaluate the importance of error handling mechanisms in C programming for interfacing with external components in embedded systems. **[7M]**

B Discuss strategies to ensure robustness and reliability in the interfacing code, especially concerning error detection and recovery. **[7M]**
