DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

QUESTION BANK

FOR

III B.TECH II SEM (R15) (2018 – 19)







MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

(Affiliated to JNTU, Hyderabad, Approved by AICTE - Accredited by NBA & NAAC – 'A' Grade, ISO 9001:2008 Certified) Maisammaguda, Dhulapally, Secunderabad – 500100.

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1	DIGITAL SIGNAL PROCSSING
2	ELECTRONIC MEASUREMENTS &
	INSTRUMENTATION
3	MICROPROCESSORS & MIROCONTROLLERS
4	DIGITAL COMMUNICATION
5	ANTENNA & WAVE PROPAGATION
6	DATA BASE MANAGEMENT SYSTEMS

Code No: R15A0415

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India) Digital Signal Processing MODEL PAPER 1

MODEL I AI EK I									
Roll No									

Time: 3 hours

Note: This question paper contains two parts A and BPart A is compulsory which carriers 25 marks and Answer all questions.Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

PART – A	(25 Marks)
(a) Distinguish between causal and non-causal system.	(2M)
(b) Find the impulse and step response of the system.	(3M)
y(n)=2x(n)-3x(n-1)+x(n-2)-4x(n-3).	
(c) Define DFT and IDFT of a discrete –time sequence.	(2M)
(d) Determine the IDFT of $X(k) = \{ 3, (2+j), 1, (2-j) \}.$	(3M)
(e) What are the properties of Bilinear Transformations?	(2M)
(f) Write the magnitude response of Chebyshev low pass filter.	(3M)
(g) What are the advantages of FIR Filter?	(2M)
(h) List the features of Rectangular Window.	(3M)
(i) Define Down sampling?	(2M)
(j) What is sampling rate conversion?	(3M)

PART – B (50 Marks)

SECTION – I

1. Check the Stability of the following systems

a) $y(n) = x(n) + \frac{1}{2}x(n-1) + \frac{1}{4}x(n-2)$ b) $h(n) = 2^n u(n)$

(**OR**)

2. Realize the system described by the following difference equation in cascade and parallel form.

$$y(n) = -\frac{13}{12}y(n-1) - \frac{9}{24}y(n-2) - \frac{1}{24}y(n-3) + x(n) + 4x(n-1) + 3x(n-2)$$
(10M)

<u>SECTION – II</u>

3. a) State and Prove any four properties of DFT.b) List the differences between linear convolution and circular convolution. (5+5M)

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Max. Marks: 75

(**10M**)

4. Find the IDFT of the sequence X(k)={7, -0.707-j0.707, -j, 0.707-j0.707, 1, 0.707+j0.707, j, -0.707+j0.707} using DIT algorithm. (10M)

SECTION – III

5. Design a digital Butterworth filter for the following specifications using impulse invariant method. (10M)

 $0.8 \le |H(w)| \le 1$ $|H(w)| \le 0.2$ $0.32\pi \le w \le \pi$

(OR)

6. Design a digital Chebyshev filter for the following specifications using bilinear transformation method. (10M)

 $0.707 \le |H(w)| \le 1$ $|H(w)| \le 0.1$ $0 \le w \le 0.2\pi$ $0.5\pi \le w \le \pi$

$\underline{SECTION-IV}$

- 7. Explain in detail about Fourier Series Method of design of FIR filters. (10M) (OR)
 a. Design of FIR filters in the series of the seri
- Design a five stage lowpass FIR filter with Sampling time 1ms and f_c=200Hz. Also find frequency response of the filter. (10M)

SECTION - V

9. Let $x(n) = \{1, 3, 2, 5, -1, -2, 2, 3, 2, 1\}$, find	
(a)Up sample by 2 times and down sample by 4 times	
(b) Down sample by 4 times and up sample by 2 times	(5+5M)
(OR)	
10. Explain the applications of Multi Rate Signal Processing.	(10M)

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India) Digital Signal Processing

MODEL PAPER 2										
Roll No										

Time:	3 hours	Max. Marks: 75
Note:	This question paper contains two parts A and B	
	Part A is compulsory which carriers 25 marks and Answer all quest	tions.
	Part B Consists of 5 SECTIONS (One SECTION for each UNIT).	. Answer FIVE Questions,
	Choosing ONE Question from each SECTION and each Question of ****	carries 10 marks.
	PART – A	(25 Marks)
1.		
(a)	Classify the Discrete Time Signals.	(2M)
(b)	Write the Z-transform of the sequence $x(n) = [3,8,2,1,-1,4]$.	(3M)
(c)	What is Zero padding? Why it is needed?	(2M)
(d)	List the Differences between Linear convolution and Circular conv	volution.
	(3M)	
(e)	Mention the important features of IIR Filters.	(2M)
(f)	How will you choose the order N for Butter worth filter?	(3M)
(g)	List the features of Hamming Window.	
	(2M)	
(h)	Write the steps involved in FIR Filter Design using windows?	
	(3M)	
(i)	What are the two basic operations in multi-rate signal processing?	(2M)
(j)	What is decimation? When it is performed?	(3M)
-	PART – B	(50 Marks)

<u>SECTION – I</u>

2.	Describe the Digital Signal Processing System and write the applications?	(10M)
	(OR)	

3. Realize the system described by the following difference equation in direct form I, Direct form – II. (10M)

$$y(n) = \frac{3}{4} y(n-1) - \frac{1}{8} y(n-2) + x(n) + \frac{1}{3} x(n-1)$$

SECTION – II

- 4. a) Find the 4-point DFT of $x(n) = \{1, -2, 3, 2\}$ b) Find the IDFT of $X(k) = \{4, -2+j4, 4, -2-j4\}$ (5+5M) (OR)
- 5. Find the IDFT of the sequence X (k) = $\{4, 1-j2.414, 0, 1-j0.414, 0, 1+j0.414, 0, 1+j2.414\}$ using DIF algorithm. (10M)

SECTION – III

Design a digital Chebyshev filter for the following specifications using bilinear transformation method. (10M)

 $0.707 \le |H(w)| \le 1$ $|H(w)| \le 0.1$ $0 \le w \le 0.2\pi$ $0.5\pi \le w \le \pi$

(OR)

Design a Butterworth digital filter using the bilinear transformation. The specification of the desired low pass filter are: (10M)

 $\begin{array}{l} 0.9 \leq \left| \, \mathrm{H}(\omega) \, \right| \, \leq 1; \, 0 \leq \omega \leq \frac{\pi}{2} \\ \left| \, \mathrm{H}(\omega) \, \right| \, \leq 0.2; \frac{3\pi}{4} \leq \omega \leq \pi \end{array}$

SECTION – IV

Design a five stage low pass FIR filter with Sampling time 1ms and f_c=200Hz. Also find frequency response of the filter. (10M)

(OR)

9. Explain in detail about design of IIR digital filters using Window Techniques. (10M)

SECTION – V

10. Explain in detail the spectrum of up sampling and down sampling.	(10M)
(OR)	

11. Consider the signal x(n)=nu(n)

- i) Determine the spectrum of the signal.
- ii) The signal is applied to a decimator that reduces the sampling rate by a factor 3.

(10M)

Time: 3 hours

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MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India) Digital Signal Processing

MODEL PAPER 3										
Roll No										

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions. Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

PART – A

(25 Marks)

1.		
(a)	Define linear shift invariant system.	(2M)
(b)	Classify the discrete-time systems?	(3M)
(c)	Write any two properties of DFT?	(2M)
(d)	What is a twiddle factor? List out some of its properties?	(3M)
(e)	What are the advantages of and disadvantages of bilinear transformation?	(2M)
(f)	What is type-1 and type-2 Chebyshev approximation?	(3M)
(g)	List the features of Triangular Window.	
	(2M)	
(h)	What are the main advantages of windowing?	
	(3M)	
(i)	State sampling theorem.	(2M)
(j)	What is interpolation?	
	(3M)	

PART – B (50 Marks)

<u>SECTION – I</u>

2. Check whether the following systems are Linear or non-linear and also verify time invariant or time variant. (10M) $i)y(n) = n^2x(n)$

ii)y(n) = n x(n)ii)y(n) = 2x(n) + 4

(**OR**)

3. Solve the following difference equation y(n)+2y(n-1)=x(n) with $x(n)=(1/3)^n u(n)$ and the initial condition y(-1)=1 (10M)

$\underline{SECTION-II}$

4. a) Find the 4-point DFT of $x(n) = \{1, -1, 2, -2\}$ b) Find the IDFT of $X(k) = \{0, -1-j, 6, -1+j\}$ (5+5M)

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Max. Marks: 75

5. Compute the 8-point DFT of $x(n) = \{1,1,1,1,1,1,0\}$ by using Radix-2 DIF FFT algorithm. (10M)

<u>SECTION – III</u>

6. Design a Butterworth digital filter using the bilinear transformation. The specification of the desired low pass filter are: (10M)

$$0.9 \le | \operatorname{H}(\omega) | \le 1; 0 \le \omega \le \frac{\pi}{2}$$
$$| \operatorname{H}(\omega) | \le 0.2; \frac{3\pi}{4} \le \omega \le \pi$$

(OR)

7. Explain Design procedure of Chebyshev and Butterworth IIR filter. (10M)

SECTION - IV

- 8. Explain in detail about design of IIR digital filters using Window Techniques. (10M) (OR)
- 9. A low pass filter has the desired frequency response as given by: (10M)

$$H_{d}\left(e^{jw}\right) = e^{-j2w} \qquad -\frac{\pi}{4} \le w \le \frac{\pi}{4}$$
$$= 0 \qquad \frac{\pi}{4} \le |w| \le \pi$$

Determine the filter coefficients if the window function is used is

 $w(n) = 1 \qquad \qquad 0 \le w \le 4$

= 0 otherwise

Also determine the frequency response $H(e^{jw})$ of the designed filter.

SECTION - V

10. Explain the applications of Multi Rate Signal Processing. (10M)

(10M)

11. Consider the signal x(n)=nu(n)

i) Determine the spectrum of the signal.

ii) The signal is applied to a decimator that reduces the sampling rate by a factor 3.

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India) Digital Signal Processing

MODEL PAPER 4										
Roll No										

Time: 3 hours

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions. Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

PART – A

(25 Marks)

Max. Marks: 75

1.	
(a) Define Static and Dynamic system.	(2M)
(b) List the applications of Digital Signal Processing.	(3M)
(c) State the difference between DITFFT and DIFFFT algorithms.	(2M)
(d) Find the IDFT of $X(K) = \{1,1,1,1\}$.	(3M)
(e) Compare impulse invariant and bilinear transformation?	(2M)
(f) Write the magnitude function of low pass Butter worth filter?	(3M)
(g) What is FIR Filter?	(2M)
(h) What are the characteristics of the window?	(3M)
(i) Define up sampling.	(2M)
(j) What are the advantages of multi-rate signal processing?	(3M)

PART – B (50 Marks)

SECTION – I

- Check whether the following systems are Linear or non-linear and also verify time invariant or time variant. (5+5M)
 - a) $y(n) = \log_{10} |x(n)|$

$$b)y(n) = x^{2}(n) + \frac{1}{x^{2}(n-1)}$$

(**OR**)

3. A linear shift invariant system is described by the difference equation (10M)

$$y(n) - \frac{3}{4}y(n-1) + \frac{1}{8}y(n-2) = x(n) + x(n-1)$$

with y(-1)=0 and y(-2)=-1.

Find a) the natural response of the system b) forced response of the system.

SECTION – II

4. Find the linear convolution of the sequence x(n) and h(n) using DFT

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 $x(n) = \{1,0,2\}, h(n) = \{1,1\}$ (10M)

- (OR)
- 5. Determine the linear convolution of the following sequences using overlap-add and overlap save methods $x(n) = \{1, -1, 2, 1, 3, 1, 2, -1, 2\}, h(n) = \{1, 2, 1\}$ (10M)

SECTION – III

6. Explain Design procedure of Chebyshev and Butterworth IIR filter. (10M) (OR)
7. Determine the system function H(z) of the lowest order Chebyshev IIR digital filter with the following specifications: (10M) 3dB ripple in pass band 0≤ w≤0.2π

25 dB attenuation in stopband $0.45\pi \le w \le \pi$

<u>SECTION – IV</u>

(10M)

(10M)

8. Design a filter with:

$$H_{d}\left(e^{jw}\right) = e^{-j3w} \qquad -\frac{\pi}{4} \le w \le \frac{\pi}{4}$$
$$= 0 \qquad \qquad \frac{\pi}{4} \le |w| \le \pi$$

using Hamming window with N = 7.

(OR)

9. Compare IIR and FIR filters in detail.

SECTION - V

10. Explain in detail the spectrum of up sampling and dow	n sampling. (10M)
(OR)	
11. Consider the signal $x(n)=a^n u(n), a <1$.	(10M)

- i) Determine the spectrum of the signal.
- ii) The signal is applied to an interpolator that increases sampling rate by a factor of 2.

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India) Digital Signal Processing

	IVI	ΙUD	EL.	PAP	EK	3		
Roll No								

Time: 3 hours

Code No: R15A0415

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions. Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

PART – A (25 Marks)

(a) What are the advantages of direct form -II structures when compared to direct form -I

		(2M)
(b)	Distinguish between periodic and aperiodic signals.	(3M)
(c)	What is the need for FFT algorithm?	(2M)
(d)	Find the DFT of $x(n) = \{1, 1, 1, 1\}$.	(3M)
(e)	Compare Butter worth and Chebyshev filters (2M)	
(f)	What are the properties of Bilinear Transformations?	(3M)
(g)	Write the important features of FIR filters.	(2M)
(h)	Write the steps involved in FIR Filter Design using windows	(3M)
(i)	What are the advantages of multi-rate signal processing	(2M)
(j)	List out the applications of multi-rate signal processing.	(3M)

PART – B (50 Marks)

SECTION - I

Check whether the following systems are Causal or non-causal and also verify Static or dynamic. (10M)

i)y(n) = x(n)x(n-2) $ii)y(n) = a^{n}u(n)$

(**OR**)

- 3. Define various elementary discrete- time signals. Indicate them graphically. (10M) <u>SECTION – II</u>
- 4. Determine the linear convolution of the following sequences using overlap-add and overlap save methods $x(n) = \{1, -2, 2, -1, 3, -4, 4, -3\}, h(n) = \{1, -1\}$ (10M)

(OR)

5. Compute the 8-point DFT of $x(n) = \{2, 2, 2, 2, 1, 1, 1, 1\}$ by using Radix-2 DIT FFT algorithm. (10M)

SECTION – III

6. Determine the system function H(z) of the lowest order Chebyshev IIR digital filter with the following specifications: (10M)
3dB ripple in pass band 0≤w≤0.2π
25 dB attenuation in stopband 0.45π≤w≤π

Max. Marks: 75

1.

7. Design a digital Butterworth filter for the following specifications using impulse invariant method. (10M)

$$\begin{array}{ll} 0.8 \le \left| H\left(w \right) \right| \le 1 & 0 \le w \le 0.2\pi \\ \left| H\left(w \right) \right| \le 0.2 & 0.32\pi \le w \le \pi \end{array}$$

SECTION - IV

8. Design a filter with:

$$H_{d}\left(e^{jw}\right) = e^{-j3w} \qquad -\frac{\pi}{4} \le w \le \frac{\pi}{4}$$
$$= 0 \qquad \qquad \frac{\pi}{4} \le |w| \le \pi$$

using Hamming window with N = 7.

9. Explain in detail about design of IIR digital filters using Window Techniques. (10M)

(OR)

<u>SECTION – V</u>

- 10. Consider the signal $x(n) = a^n u(n)$, |a| < 1.
 - i) Determine the spectrum of the signal.
 - ii) The signal is applied to an interpolator that increases sampling rate by a factor of 2.

(OR)

11. Let $x(n) = \{1, 3, 2, 5, -1, -2, 2, 3, 2, 1\}$, find

(a)Up sample by 2 times and down sample by 4 times(b) Down sample by 4 times and up sample by 2 times

(OR)

(10M)

(**10M**)

(**10M**)

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

III B.Tech II Semester supplementary Examinations, Nov/Dec 2018

Electronic Measurements & Instruments

		(E0	CE)			
Roll No						

Time: 3 hours

Note: This question paper contains two parts A and B
 Part A is compulsory which carriers 25 marks and Answer all questions.
 Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

PART – A

(25 Marks)

(50 Marks)

Max. Marks: 75

- 1. (a) List any four static characteristics of a measuring system. [2M]
 - (b) What is an error? What is calibration? [3M]
 - (c) What are the general requirements of signal generator? [2M]
 - (d) Give few useful applications of spectrum analyzer. [3M]
 - (e) What is a Lassajous Figure? [2M]
 - (f) Explain what is meant by the deflection sensitivity of a CRO? [3M]
 - (g) What is meant by gauge factor of a strain gauge. [2M]
 - (h) What are active and passive Transducers?. [3M]
 - (i) Define data acquisition system. [2M]
 - (j) Write the applications of Wheatstone bridge. [3M]

PART – B SECTION – I

2. a) Explain the Performance characteristics of measurement system. [6M]

b) Explain about AC voltmeters in detail. [4M]

(OR)

3. a) With a neat diagram explain in detail the construction of Multimeter. [5M]b) What are the different types of errors in measurement? Explain. [5M]

<u>SECTION – II</u>

4.a) Describe the working of Heterodyne wave analyzer with the block diagram [5M]b) Describe the working of a pulse and square wave generator. [5M]

(OR)

- 5. Explain with neat diagrams, the working of the following: [2*5=10]i) Spectrum analyzer
 - ii) RF Signal generator.

<u>SECTION – III</u>

6. a) Draw the block diagram time base circuit and explain its operation. [5M]b) Explain block diagram of Dual Beam Oscilloscope with neat diagram. [5M]

(OR)

7. a). Draw the block diagram of a CRO and explain the function of each block. [5M]b) Draw and explain about Storage Oscilloscope with neat diagram. [5M]

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SECTION – IV

- 8. a) Explain the hotwire anemometer. [4M]
 - b) Explain the construction and operation of Resistance Thermometer and write its applications. [6M]

(OR)

- 9. a) Explain the construction and operation principles of Linear Variable Differential Transformer (LVDT). [6M]
 - b) Explain Magnetostrictive Transducers and write its merits, demerits and applications. [4M] <u>SECTION – V</u>
- 10. a) Draw a circuit diagram of Kelvin bridge and derive the equation for determining unknown quantities. [5M]
 - b) Describe with suitable sketches, various basic schemes for measurement of pressure.[5M]

(OR)

- 11. a) Explain the data acquisition system with neat block diagram. [5M]
 - b) Draw neat sketch and describe the construction of a capacitive transducer for measurement of liquid level. [5M]

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India) III B.Tech II Semester Regular Examinations, April/May 2018

Electronic Measurements & Instruments

(ECE)

Roll No	
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Time: 3 hours

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions.

Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

	PART – A	(25 Marks)
1.	(a) What are the sources of errors in DC voltage measurement?	[2M]
	(b) Define the term accuracy and term precision.	[3M]
	(c) What is meant by harmonic distortion.	[2M]
	(d) Draw the block diagram of basic wave analyzer.	[3M]
	(e) Explain what are the essential components of a CRT.	[2M]
	(f) Write the applications of CRO.	[3M]
	(g) Define Force and displacement Transducers.	[2M]
	(h) Explain Magnetostrictive Transducers.	[3M]
	(i) Write the applications of Kelvin bridge.	[2M]
	(j) What is the difference between pressure and vacuum pressure.	[3M]

PART – B

(50 Marks)

<u>SECTION – I</u>

2.	a) Explain the static and dynamic characteristics of an instrument.	[6M]
	b) What are the different types of errors in measurement? Explain.	[4M]

(**OR**)

3.	3.	
	a) With a neat diagram explain in detail the con	struction of DC voltmeter using PMMC
	Instrument.	[6M]
	b) How do you measure large currents in PMMC	. [4M]

SECTION - II

4. Describe the working of function generator with the block diagram. [10M]

(OR)

5. a) Describe the working of a sweep frequency generator.[5M]b) Explain the distortion analyzer with the help of suitable diagrams.[5M]

SECTION – III

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Max. Marks: 75

6.	a) Draw the block diagram of a CRO and explain the function of each block.	[5M]
	b) Explain block diagram of Dual Trace Oscilloscope with neat diagram.	[5M]

(OR)

7.	a) Draw the block diagram of CRT and explain each part. Explain the how	would you
	measure frequency using CRO.	[5M]
	b) Draw and explain about Sampling Oscilloscope with neat diagram.	[5M]

SECTION - IV

8. a) Explain Bounded and unbounded wire strain gauges. [4M]b) Explain the construction and operation of Thermocouples and write its applications.

(OR)

[6M]

9	a) Explain the construction and operation principles of Linear Variable Differential	
	Transformer (LVDT).	[6M]
	b) Explain Piezoelectric Transducers.	[4M]

SECTION - V

10. Draw a circuit diagram of Wheat stone bridge and derive the equation for determining unknown quantities and also calculate current at unbalanced condition. [10M]

(OR)

11. a) Describe with suitable sketches, various basic schemes for measurement of	vacuum
Pressure.	[5M]
b) Explain the Measurement of Humidity and Moisture.	[5M]

Code No: 115AM JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD **B.Tech III Year I Semester Examinations, March - 2017** ELECTRONIC MEASUREMENTS AND INSTRUMENTATION (Electronics and Communication Engineering)

Time: 3 hours

Note: This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

PART - A

1.a) Define Accuracy. [2] What is meant by D'Arsonval Movement? b) [3] What is meant by Harmonic Distortion? [2] c) What is Sweep frequency generator? d) [3] What is the function of electron gun? e) [2] List the different control knobs available on the front panel of the CRO. f) [3] Define Gauge factor. **g**) [2] What is the purpose of Hotwire Anemometer? [3] h) List the advantages of Wheatstone bridge. i) [2] What is the method for the measurement of Liquid level? j) [3]

PART - B

2.a) Discuss briefly the different types of static errors of a measuring instrument. Explain the working of a true RMS voltmeter with the help of a suitable block diagram. b) [5+5] OR 3.a) Explain the working of Ramp type DVM. Discuss the advantages of a digital voltmeter over an analog voltmeter. b) [5+5] 4.a) Discuss the working of spectrum analyzer with its basic circuit. Explain the working of Power Analyzer with a neat diagram. b) [5+5] OR With a neat diagram explain the working of Pulse and Square wave generator. 5.a) Explain the working of Capacitance-Voltage meter. b) [5+5] 6.a) Briefly explain the different types of storage oscilloscopes. What is the role of Time base generator? Explain. b) [5+5]OR What is sampling oscilloscope? Mention its advantages and disadvantages. 7.a)

Explain how time and frequency is measured using CRO. b) [5+5]

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Max. Marks: 75

(25 Marks)

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(50 Marks)

8.a) What are the factors to be considered for the selection of better transducer? Explain.

[5+5]

- b) Explain the principle and working of an LVDT. OR
- 9.a) What is a transducer? Explain the working of Variable Capacitance transducer.
- b) A 100 Ω strain guage with a guage factor of 1 is affixed to a metal bar. The bar is stretched and this causes a change in resistance of 0.001 Ω . Find the change in length if the original length is 10cm. [5+5]
- 10.a) With a neat diagram explain the working of Maxwell Bridge.
 - b) Explain the methods for the measurement of temperature. [5+5]

OR

- 11.a) What are the limitation of Wheat stone's bridge? Derive the balance equation of Kelvin's double bridge for unknown low resistance.
 - b) Discuss the principle of working of Displacement meters. [5+5]

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Code No: 115AM JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year I Semester Examinations, November - 2015 ELECTRONIC MEASUREMENTS AND INSTRUMENTATION (Electronics and Communication Engineering)

Time: 3 hours

Max. Marks: 75

Note: This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks.

PART - A (25 Marks)

1.a)	Distinguish between accuracy and precision.	[2]
b)	Enumerate the salient features of a measuring system.	[3]
c)	What is Barkhausen Criteria for sustained oscillation?	[2]
d)	Draw the block diagram of spectrum analyzer.	[3]
e)	What will happen when sweep signal is applied to horizontal plates of CRO?	[2]
f)	Draw the internal structure of CRT and list its functions.	[3]
g)	What are the factors to be considered for selections of transducers?	[2]
h)	What are the applications of LVDT?	[3]
i)	Give the significance of Kelvin Bridge.	[2]
j)	Write about velocity measurement system.	[3]

PART - B (50 Marks)

2.a)	Describe the Operating Principle involved in the integrating type digital voltmeter with a neat block diagram.	1
b)	List out the advantages of Digital Voltmeter over other voltmeters. [6+4	1]
	OR	
3.a)	How do you extend the range of a given ammeter and voltmeter?	
b)	Explain different types of errors in digital voltmeters. [5+5	5]
4.a)	State the application of a spectrum analyzer.	
b)	Draw the block diagram of a distortion measuring component type meter and explain it	ts
	working. [5+5	5]
	OR	
5.	Discuss the following with neat block diagram.	
	a) Pulse wave generator	
	b) Square wave generator. [5+5	5]
6.	How does the Digital storage Oscilloscope differ from the conventional storage oscilloscope using a storage cathode ray tube? What are the advantages of each? [10]	ge)]

OR

7. Discuss about the electrostatic focusing deflection system of a CRO with necessary diagrams. [10]

8. What is meant by Piezo electric transducer? Explain its working with a neat block diagram. [10]

OR

- 9. Describe the construction and working of potentiometer type resistance transducer for measuring linear displacement. [10]
- 10. Draw the block diagram of analog data acquisition system and explain the function of the components. [10] OR
- 11. Explain how the Humidity and Moisture are measured. [10]

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Code No: 115AM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year I Semester Examinations, November/December - 2016 ELECTRONIC MEASUREMENTS AND INSTRUMENTATION (Electronics and Communication Engineering)

Time: 3 hours

Note: This question paper contains two parts A and B.

Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

(25 Marks)

1.a)	Define accuracy and precision.	[2]
b)	What is loading effect in voltmeter?	[3]
c)	Compare Moving coil with Moving iron instruments.	[2]
d)	Draw the internal structure of CRT and list its functions.	[3]
e)	Draw the block diagram of spectrum analyzer.	[2]
f)	What are the advantages of digital instruments over analog instruments?	[3]
g)	Draw the block diagram of Digital Data Acquisition System.	[2]
h)	Name the different temperature sensors and their advantages.	[3]
i)	State the LVDT principle.	[2]
j)	Explain the procedure of air-flow measurement.	[3]

PART - B

(50 Marks)

- 2.a) Explain about source for different types of errors and precautions to minimize them.
 - b) The accuracy of five digital voltmeters are checked by using each of them to measure a standard 1.0000V from a calibration instrument. The voltmeter readings are as follows: $V_1=1.001v$, $V_2=1.002v$, $V_3=0.999v$, $V_4=0.998v$ and $V_5=1.0000v$. Calculate the average measured voltage and the average deviation. [5+5] OR
- 3.a) With a neat diagram, explain the working of a True RMS responding volt meter.
 - b) A PMMC instrument has FSD of $100 \,\mu$ A and a coil resistance of $1K_{\Omega}$. Calculate the required shunt resistance value to convert the instrument into an ammeter with (i) FSD=100mA and (ii) FSD=1A. [5+5]
- 4.a) What is the principle of harmonic distortion analyzer? Explain its operation with the help of a functional block diagram.
- b) Compare the selectivity characteristics of the Spectrum Analyzer and Heterodyne Wave Analyzer. [5+5]

OR

5.a) With a neat sketch explain the operation of a heterodyne type wave analyzer.
b) Explain the following www.assMieten aiR@stunl ApgyzeCo.in
i) Sensitivity ii) Dynamic Range iii) Harmonic Mixing

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Max. Marks: 75

[5+5]

- 6.a) Explain how Lissajous figures are used to determine the characteristics of unknown input. Show how to estimate input if the pattern is (i) Circle (ii) Ellipse (iii) Parabola.
 - b) Derive an expression for electrostatic deflection sensitivity of a CRO. [5+5]

OR

- 7.a) Draw the block diagram of storage oscilloscope and explain the function of each block.
- b) Derive the expression for vertical deflection of electron beam in CRT. [5+5]
- 8.a) Explain how LVDT is used to measure linear displacement.
- b) Show that a parallel plate capacitor serves as the most suitable transducer for measurement of linear and angular displacements. [5+5]

OR

- 9.a) Show that a parallel plate capacitor serves as the most suitable transducer for measurement of linear and angular displacements.
 - b) A transducer that measures force has nominal resting resistance of 300 Ω and is excited by 7.5V. When a 980 dyne force is applied, all four equal resistance bridge elements change resistance by 5.2 Ω . Find the output voltage E₀. [5+5]
- 10.a) The basic AC bridge consists of the following constants: AB: R=400 Ω , BC: R=150 Ω , CD: unknown and DA: R=100 Ω in series with L=10mH. Oscillator frequency is 1KHz. Determine the constants of arm CD.
 - b) What is Wien's bridge? Derive the expression for the frequency. [5+5]

OR

- 11.a) Explain different methods of liquid level measurements.
 - b) Explain different steps adopted by a controller in data acquisition in asynchronous Mode. When asynchronous method of data acquisition is required. [5+5]

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MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

III B.Tech II Semester supplementary Examinations, Nov/Dec 2018

Microprocessors and Microcontrollers

(ECE)										
Roll No										

Time: 3 hours

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions.

Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions,

Choosing ONE Question from each SECTION and each Question carries 10 marks.

PART – A (25 Marks)

1.

- a) What is the purpose of segment registers in 8086? [2M]
- b) What is the need of a flag register in 8086. [3M]
- c) Define Procedure. Write down the syntax & example of Procedure definition [2M]
- d) Classify the Instruction sets available in 8086. [3M]
- e) What are the basic modes of operation of 8255? [2M]
- f) State the cascading signals of 8259programmable interrupt controller. [3M]
- g) Compare Microprocessor and Microcontroller. [2M]
- h) What are the different group of instructions supported by 8051. [3M]
- i) Draw the diagram for Interrupt Priority register. [2M]
- j) Draw the diagram for TCON and TMOD register. [3M]

PART – B

(50 Marks)

<u>SECTION – I</u>

2. a) Explain the register organization of 8086 microprocessor. [5M]

b. What is memory segmentation ? Explain the use of segmentation in different applications. [5M]

(**OR**)

3.a)With a neat sketch describe the Minimum and Maximum mode of operation of 8086. [5M]

b. Discuss in detail the various signals of 8086. [5M]

<u>SECTION – II</u>

4.a) Write an 8086 assembly language program to sort an array of data in descending order. [5M]b.) Discuss about the different Address Modes of 8086. Give Example for each type [5M]

(**OR**)

5.a) Write an 8086 assembly language program to multiply two 8 bit binary numbers. [5M]b.) Explain in detail about the Stack Structure of 8086. Write a sample program to illustrate the concept of programming the stack. [5M]

<u>SECTION – III</u>

6.a) Explain about I/O Interfacing and Memory Interfacing with 8086 with an example? [5M]b.) Draw the Block diagram and explain the operations of 8251 serial communication interface? [5M]

(OR)

7.a) Draw the Block diagram of 8255 and explain the functions of each block. [5M]

b.) Draw the Block diagram of 8257 DMA controller and explain its operations. [5M]

Max. Marks: 75

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SECTION – IV

8.a) With the functional block diagram, explain the architecture of 8051 microcontroller. [5M]b.) Write an Assembly Language Program using 8051, i)Addition of two 8 bit Numbers ii). Multiplication of two 8 bit Numbers [5M]

(**OR**)

9.a) Draw the Pin diagram of 8051 Microcontroller and explain the functions of each pin. [5M]b.) Discuss in detail about the Addressing Modes of 8051 Microcontroller. [5M]

<u>SECTION – V</u>

10.a) Explain about the Timers of 8051 with its Modes of Operation, and the Registers used for 8051 Timers. [5M]

b.) What are the interrupts available in 8051? Explain about the Interrupt Structure. [5M]

(**OR**)

11. Explain about the Serial data communication of 8051 with its registers. Also explain about the Modes of operation of the same. [10M]

Code No: 126EM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, December - 2017 MICROPROCESSORS AND MICROCONTROLLERS (Common to ECE, ETM)

Time: 3 hours

Max. Marks: 75

Note: This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

PART - A

(25 Marks)

1.a)	Which type of operation indicated by status lines of 8086?	[2]
b)	What are the capabilities of I/O address lines of microprocessor?	[3]
c)	Define the term macro's.	[2]
d)	Give the advantages of assembly language over machine language.	[3]
e)	Define the term Interrupt.	[2]
f)	List out the difference between static and dynamic memories.	[3]
g)	What is the function of Port 3 of 8051 microcontroller?	[2]
h)	What is the function of clock generator of 8051? What are the signals are used	for clock
	in 8051.	[3]
i)	What is the function of Timer?	[2]
j)	How does effect the SBUF SFR in serial communications of 8051?	[3]

PART - B

(50 Marks)

2.a)	Draw and explain the each bit of flag register of 8086 family microprocessor.
b)	Describe the implementation of pipelined process of 8086. [5+5]
	OR
3.a)	Draw and explain the read and write cycle timing diagrams of 8086 in maximum mode.
b)	Explain the physical memory organization of 8086 system. [6+4]
4.	Enlist the addressing modes of 8086 and describe briefly each addressing mode with one example. [10]
	OR
5.	Explain the all assembler directives of 8086 with suitable examples. [10]
6.a)	Interface Eight 8K RAM chips and Four 8K×4 EPROM chips with 8086 so as to form a completely working system configuration.
b)	Explain the interfacing procedure of an 8-bit ADC with 8086 microprocessor. [5+5]
7.a)	Explain the briefly the different modes operation of 8255 PPL
b)	Draw and explain the synchronous mode transmitter and receiver data formats of 8251.
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8. Draw and explain the internal architecture of 8051 family microcontroller and explain each block of it. [10]

OR

- 9.a) Describe briefly the register set of 8051 microcontroller.
- b) Explain the importance of data transfer type instructions of 8051. [5+5]
- 10.Draw and explain the following SFRs.[5+5]a) IEb) IP

OR

- 11.a) Write and explain the instructions to read the SBUF eight times with an interval of 0.33ms and save the results between the R0 and R7 of the register bank 0.
 - b) How does the timer overflow interrupt differ from real time clocked interrupts? Discuss in detailed. [5+5]

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, May - 2017 MICROPROCESSORS AND MICROCONTROLLERS (Common to ECE, ETM)

Time: 3 hours

Note: This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

PART - A

(25 Marks)

1.a)	Name the fourteen registers used for 8086 microprocessor?	[2]
b)	Draw the flag register bits in 8086 microprocessor.	[3]
c)	Write a program to find the factorial of 4 using 8086 microprocessor.	[2]
d)	What is the purpose of using macro's?	[3]
e)	What are the different memories used to interface 8086 microprocessor?	[2]
f)	Give the serial communication standards.	[3]
g)	Write about PSW used in 8051 microcontroller.	[2]
h)	Write TCON special function register used in 8051 microcontroller.	[3]
i)	What are different types of interrupts are used in 8051 microcontroller?	[2]
j)	Draw the timer/counter control logic diagram in 8051 microcontroller.	[3]

PART - B

(50 Marks)

2.a) b)	Explain about the architecture of 8086 microprocessor with a neat sketch. Draw the timing diagram used for minimum mode of chip diagram of microprocessor.	8086 [5+5]
	OR	
3.a)	Explain signal description of 8086 microprocessor.	
b)	What is an interrupt? Explain various types of interrupts in 8086 microprocessor.	[5+5]
4.a)	Explain various addressing modes of 8086 microprocessor.	
b)	Describe the instruction set of 8086 microprocessor:	[5+5]
,	i) SHR ii) ADD iii) DAA iv) CMP v) CBW	
	OR	
5.a)	Write a program for finding multibyte addition using 8086 microprocessor.	
b)	Write a program to print a string 'MICRO' using 8086 microprocessor.	[5+5]
6.a)	Describe the interfacing of D/A Converter with a neat sketch.	
b)	Explain about the mode 2 operation used in 8255 PPI in detail.	[5+5]
	OR	
7.a)	Explain about the architecture of 8251 USART with a neat sketch.	

b) Write a short notes **WWW**che **ManaRasia** [5+5]



Max. Marks: 75

8.a)	Write an	overview	of 8051	microcontrollers.
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b) Write a program to find 1's and 2's complement of a number using 8051 microcontroller. [5+5]

OR

- 9.a) Write a program to find 8-bit subtraction using 8051 microcontroller.
 - b) Write a program to add 16 bit numbers using 8051 microcontroller. [5+5]
- 10.a) Describe the timer mode 0 used in 8051 microcontroller with a neat sketch.
 - b) Describe the timer mode 1 used in 8051 microcontroller with a neat sketch. [5+5]

OR

11.a) Describe the timer mode 3 used in 8051 microcontroller.b) Write a short notes on external interrupts in 8051 microcontroller. [5+5]

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Code	No: 126EM JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, April - 2018 MICROPROCESSORS AND MICROCONTROLLERS (Common to ECE, ETM) 3 hours Max. Marks: 75
Note:	This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions. PART- A (25 Marks)
1.a) b) c) d) e) f) g) h) i) j)	Advantal Server (Common to ECE, ETM) R13 JAWAHALAN CHIRU TECHNOLOGICAL UNIVERSITY HUDERABAD B. Tech III Year (ISemseter Examinations, Appil - 2018) MICROPROCESSORS AND MICROCONTROLLERS (Common to ECE, ETM) Max. Marks: 75 Time: 3 hours Max. Marks: 75 Note: This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions. PART-A (25 Marks) 1.a) When does the 8086 processor is in minimum mode and maximum mode? [2] b) List different togical instructions of 8086. [2] c) Give the advantages of assembly language over machine language. [3] c) Write the difference togical instructions in 8051? [3] c) Huat is push and POP instructions in 8051? [3] j) What is the difference between microprocessor and micro controller? [3] j) Wut what is the BUF SFR in serial communications of 8051? [3] j) How does effect the SUP SFR in serial communications of 8051? [4] j) How does effect the SUP SFR in serial communications of 8051? [5] j) How does effect the SUP SFR in serial communications of 8051? [5] j) How does effect the SUP SFR in serial communications of 8051? </th
2.a) b) 3.	Explain the concept of segmented memory. What are the advantages? Describe the implementation of pipelined process of 8086. [5+5] OR Explain the internal hardware architecture of 8086 microprocessor with neat diagram. [10]
4.a) b) 5.a)	Write an 8086 ALP to find the sum of numbers in the array of 10 elements. Explain any five assembler directives of 8086 with suitable examples. [5+5] OR Write an assembly language program (ALP) which counts the number of A's and a's in
b)	a string of characters Explain the function of the following instructions. i) AAD ii) MOVSB iii) LAHF iv) JNZ v) LEA v) DAD Explain the briefle the different we do execution of 8255 DDI
6.a) b)	Explain the briefly the different modes operation of 8255 PPI. Draw and explain the synchronous mode transmitter and receiver data formats of 8251. [5+5]

······

7.a)	Write a progra I/O base addre Explain the in	am to interface 4 esses 0FFF9. Dra iterfacing proced	OR ×4 keyboard to 8 w the necessary ure of an 8 - bit I	086 through port interface details. DAC with 8086 r	s A and B opera	ting at [5+5]
8.a) b) 9.a)	Explain SCOl Write an AI programming Explain the I/	N register progra LP to generate O pins ports and	mming in 8051. the 1 kHz squ OR circuit details of	are wave form 8051 with its dia	using mode	1 timer [5+5]
b) 10.a) b)	Write a progra result in R4 an Explain: i) TC Discuss about	am to multiply th nd R5 registers. CON ii) TMOD r t 8051 serial port	egisters in detail. programming.	ocation 3AH by t	he number 11H.	[5+5]
11.a) b)	How does 805 How does tim	51 process generation over flow inte	ate the ISR addre	ss on an un-mark n real time clocke	ed interrupt? ed interrupts?	[5+5]
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MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

III B.Tech II Semester Regular Examinations, April/May 2018

Microprocessors and Microcontrollers (ECE)

Roll No					

Time: 3 hours

Max. Marks: 75

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions. Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

PART – A

(25 Marks)

1

- a) What is the use of Instruction Pointer in 8086? (2M)
- b) What are the two modes of operations present in 8086? (3M)
- c) Differentiate Macros and Procedures. (2M)
- d) What are the types of Addressing modes of 8086? (3M)
- e) What is an USART? (2M)
- f) Give the Modes of operation of 8255? (3M)
- g) List the features of 8051 microcontroller? (2M)
- h) Write the difference between microprocessors and microcontroller. (3M)
- i) Describe the timer modes of operation. (2M)
- j) Explain the pipe line concept of ARM processor.? (3M)

(50 Marks)

SECTION – I

PART – B

2. Explain the architecture of 8086 with neat diagram. (10M)

(**OR**)

3. Explain the function of following registers 8086 microprocessor. a) AX,BX,CX,DX b) CS,DS,SS, ES c) BP,SP, SI& DI d) IP and instruction queue (10M)

SECTION – II

4. Explain the instructions of 8086 with examples. (10M)

(OR)

5.a) Write an 8086 assembly language program to convert Binary to BCD number? (5M)

b.) Describe in detail about the Procedures with suitable syntax and example. (5M)

SECTION - III

6. Draw the Block diagram and explain the operations of 8255 PPI. (10M)

(**OR**)

7. Explain the architecture of 8251A with neat diagram. (10M)

SECTION – IV

8. a) Explain the architecture of 8051 microcontroller. (5M)

b.) Explain in detail about Ports of 8051. (5M)

(**OR**)

9. a)Explain about the Memory Structure of 8051. (5M)b.)Write an Assembly Language Program using 8051, i)Addition of two 8 bit Numbers ii). Addition of two 16 bit Numbers? (5M)

SECTION – V

10 Describe the various timers/ counters of 8051. (10M)

(OR)

11.(a) Explain about the CPSR register of ARM processor (5M)

(b). Explain about data flow model of ARM processor. (5M)

R15

Code No: R15A0413

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

III B.Tech II Semester Regular Examinations, April/May 2018

Digital Communications



Time: 3 hours

Max. Marks: 75

R15

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions.

Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

******** PART – A

(25 Marks)

- 1. (a) What is the function of predictor in DPCM system? [2M]
 - (b) Compare PCM and DM. [3M]
 - (c) Mention the advantages of BPSK . [2M]
 - (d) Explain A- law and μ-law of companding in PCM systems .[3M]
 - (e)Define mutual information and Entropy [2M]
 - (f)Prove that I(X;Y) = H(X)+H(Y)-H(X,Y) [3M]
 - (g) Mention the properties of cyclic code. [2M]
 - (h) Explain about the syndrome calculation. [3M]
 - (i) Compare between linear block codes and convolutional codes [2M]
 - (j) Write the difference between code tree and Trellis diagram [3M]

PART – B

(50 Marks)

SECTION - I

2. (a)State and prove the sampling theorem for band limited signals. [5]

(b) What is the difference between uniform and non-uniform quantization. [5]

(OR)

3. Draw the Delta modulation system? And Explain noise in DM. [10]

SECTION - II

- 4. (a) Draw and explain the operating principles of ASK Modulator. [5]
- (b) Explain the DPSK modulation technique with help of neat diagram [5]

(OR)

5. Explain the coherent detection of FSK and also derive the expression for probability of error. [10]

SECTION - III

6. (a) Explain about the bandwidth and signal to noise ratio trade off? [5]

(b)Define conditional entropy. Explain how it is related to mutual information. [5]

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7. Given six messages X_1 , X_2 , X_3 , X_4 , X_5 , X_6 with probabilities $P(X_1)=1/3$, $P(X_2)=1/4$, $P(X_3)=1/8$, $P(X_4)=1/8$, $P(X_5)=1/12$, $P(X_6)=1/12$. Find the Shannon-Fano code. Evaluate the coding efficiency. [10]

SECTION - IV

8. Consider the (15,9) cyclic code generated by G(p) = p6+p5+p4+p3+1, draw the encoder and Syndrome calculator. [10]

(OR)

9. (a) Briefly discuss about a linear block code error control technique .[5] (b) Briefly discuss about Parity check bit error control technique. [5]

SECTION - V

For the convolutional encoder shown in figure 2, determine the following:

- a. Dimension of the code
- b. Constraint length

10.

- c. Generating sequences (impulse responses)
- d. Output sequence for the message sequence of $m = (1 \ 0 \ 0 \ 1 \ 1)$



Fig 2: Convolutional Encoder

11. Explain generation of convolutional code using an example. Also explain the term constraint length and its significance in convolutional code. [10]

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[3]

[3]

[2]

[2]

Code No: R15A0413

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

III B.Tech II Semester supplementary Examinations, Nov/Dec 2018

Digital Communications



Time: 3 hours

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions.

Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

**** PART – A

(25 Marks)

Max. Marks: 75

R15

- 1. (a) Discuss the drawbacks of DM System [2M]
 - (b) Write the advantages of digital communication [3M]
 - (c) Compare QPSK and other digital modulation schemes [2M]
 - (d) Define base band transmission [3M]
 - (e)What is Hartley Shannon law? [2M]
 - (f) What is aliasing effect? [3M]
 - (g) Explain about error correction and error detection in (n, k) cyclic codes [2M]
 - (h)Explain the principle of linear block codes [3M]
 - (i) Write the advantages of convolution codes [2M]
 - (j)Compare between Code tree and Trellis diagram [3M]

PART -B

(50 Marks)

<u>SECTION – I</u>

(a) Derive the expression for signal to quantization noise ration in PCM system. [6M]
 (b) Explain DPCM transmitter with a neat deiagram . [4M]

(OR)

3. (a) Draw and explain the elements of digital communication system? [7M](b) List out the applications of DPCM[3]

SECTION - II

4. (a)Derive an expression for error probability of non-coherent ASK scheme. [5M]
(b) Describe the BPSK modulation technique with help of neat diagram. [5M]

(OR)

- 5. (a) Explain the working of non-coherent FSK detector.[5M]
 - (b) Explain optimum filter using correlator . [5M]

SECTION - III

6. A source emits different symbols a, b, c, d, e with respective probabilities 0.1, 0.2, 0.1, 0.1, 0.5. Obtain the code words using Huffman coding and also calculate entropy. [10M]

(OR)

7. Explain the concept of amount of information and its properties. And prove at least three properties. [10M]

SECTION - IV

8. Design a syndrome calculator for a (7, 4) cyclic Hamming code generated by the polynomial G (p) = $P^3 + P + 1$. Calculate the syndrome for Y= (1001101). [10M]

(OR)

9. (a)Explain how to decode cyclic codes.[5M]

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(b) Give the matrix description of linear block codes. [5M]

<u>SECTION - V</u>

- 10. Draw the state diagram, tree diagram, and trellis diagram for k= 3, rate=1/3 code generated by $g_1(x) = 1+x^2$, $g_2(x) = 1+x$, $g_3(x) = 1+x+x^2$. [10M] (OR)
- 11. (a)Explain the decoding of convolution codes using Viterbi algorithm.[7M](b) Discuss about tree and state diagram.[3M]

R	13
Code No: 126AN	D
B Tech III Year II Semester Examinations, May - 2016	
DIGITAL COMMUNICATIONS	
(Electronics and Communication Engineering)	
Time: 3 hours Max. Ma	rks: 75
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Note:"This question paper contains two parts A and B."	
Part A is compulsory which carries 25 marks. Answer all questions in Part A.	Part B
consists of 5 Units. Answer any one full question from each unit. Each question	carries
10 marks and may have a, b, c as sub questions.	κ.*
PART - A (25 Marks)	BH ME
1 a What are the drawbacks of delta modulation?	[2]
b) Explain the need for non-uniform quantization in digital communication.	[3]
c) Draw the Signal space Diagram of ASK.	[2]
d). List out the Advantages of Pass band Transmission over Baseband transmission.	[3]
e) Define Entropy	[2]
f) Derive the Expression for the Information Rate.	[3]
g) Explain in one sentence about (i) Block Size (ii) Linear block codes.	[2]
h) List out Properties of Cyclic Codes.	[3]
i) Briefly explain about "Spread spectrum."	[2]
j)What is Frequency hopping spread spectrum?	[3]
PART - B (50 Marks)	· .
2.a) With neat block diagram, Explain the process of Sampling and Quantization 1 communication.	n digital
b): Derive the expression for the Quantization error	[5+5]
3.a) Explain about the noise in PCM systems.	
b) Write the comparison between PCM and Analog modulation techniques.	[5+5]
4.a) With neat diagrams and equations, explain about PSK system.	1. 15 151 1. 1. 1.
b) Draw the space representation of BPSK. And also draw its wave to fills?	
UN	encoded
5.a) The bit stream for 1011100011 is to be transmitted using brok. Determine the	cheoded
b) Explain about DPSK system And also give the comparison between DPSK and	1 PSK.
b) Explain about DI BIX System. This also give and comparison converted 2 a converted and	[5+5]
이 지수는 지수는 지수는 지수는 지수는 것을 수가 있다.	
6.a) What is the need of pulse shaping for optimum transmission in baseband transmission	nission?
Explain.	
b) What is meant by Cross talk? Explain in detail about the causes for cross talk.	[5+5]
OR	
7.a),Briefly explain about Variable length coding.	
b)::Explain in detail about Huffman coding and Lossy source code:	···.[5+5]

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	 8.a) ::Write b) Explained 9.a) Explained b) For a i) The iii) All 10.a) Explained b) Give a 11.a) Explained b) Sive a 	short notës on Ha in about Error det in how Parity che linear block code Syndrome depen error patterns tha in the role of code brief history about in about PN-Sequ is meant by S um? Explain in d	amming codes. ection and Correct cking can be use prove with exan ds only on error at differ by a cod e division multip out direct sequen (nences generation? etail.	ction capabilities R d for error detect nple that: pattern and not of eword have the s le access technic ce spread spectru DR n and their charac Why we require	s of Hamming coo tion or error corre on transmitted coo same syndrome? que in present gen um. cteristics hire synchronizat	des. [5+5] ction. le word? [5+5] eration? [5+5] ion [n]	NO NO
			00	000			
	ЫЭ	НЗ			N3	H3	
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B.Tech III Year II Semester Examinations, October/November-2016 DIGITAL COMMUNICATIONS

Time:	3 hours (Elec	tronics and Com	nunication Engin	eering) Max	. Marks: 75	
Note:	This question paper co Part A is compulsory consists of 5 Units. Ar 10 marks and may hav	ntains (wo parts) which carries 25 swer any one ful e a, b, c as sub q	A and B: marks. Answer Il question from e uestions.	all questions in Pa each unit. Each que	A. Part B estion carries	NS
	3 N3	PAI	₹T - A	NG. 4	(25 Marks)	
1.a) b) c) d) e) f) g) h) i) j)	Compare PCM and DI Write the advantages of Define QPSK. Draw the block diagra Define Basebaüd trans Define conditional ent Mention the properties Write the advantages of List out the application Define spread spectrum	M. of digital commun m of the P.L. smission ropy. s of cyclic code. of convolution co ns of CDMA. m. List its uses. PAI	nication.		[2] [3] [2] [3] [2] [3] [2] [3] [2] [3] [2] [3] [2] [3] [2] [3] [2]	NO NO
2.a) b): ∙	What is Hartley Shann With a neat sketch des	non law? And exp scribe ADPCM co	plain sampling the		[NB
3.a) b)	Explain the tradeoff b Distinguish between a	etween bandwidt nalog communic	h and signal to no ation and digital	oise ratio. communication.	[5+5]	
4.a) b)	Draw and explain the Describe the BPSK m	operating princip odulațion technic	le of ASK Modu jue with the help OR	lator. of a neat diagram.	[5+5]	NS
5.a) b)	Explain the DPSK mo Explain the working o	dulation techniqu f non-coherent F	e with the help on the self of	of a neat sketch.	[5+5]	
6.a) b)	Draw and explain the Define eye diagram. D	working of optim Draw the eye diag	num receiver with ram for FSK. OR	a neat diagram.	[5+5]	N3
7.a) b)	Explain Huffman codi Explain crosstalk conc	ng with an exam cept.	ple.		[5+5]	

	8.a) :Descrit b) · ····Explain	be the algebraic s	trucluțe of cyclic cyclic codes.	$\left r_{1} \right \frac{r_{11}^{(1)}}{r_{11}}$	(, , , '''[545]			
	9.a) Give the b) Decode	OR) Give the matrix description for linear block codes,) Decode convolution process using viterbi algorithm,						
8	10:a) What a b Descri	re the process of	stics of PN seque code division m	encesi?, Explain ultiple access in a	$\left \frac{1}{1}, \frac{1}{1}, \frac{1}{1}, \frac{1}{1} \right $	[5+5]		
	11.a) Describ) Descri	be with a neat sk be the concept of	C etch the direct se Ranging using !	PR Equence Spread sj DSSS,	pectrum techniqt	ie. [5+5]		
	МЗ	NЗ	N.3					
			00	000				
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	H3	NB	H3	HB	H			
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3	43	ЫЗ	H3	НЭ	NЗ	НЗ	NE	
3	NB	МЭ		НЭ		NC		

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MALLA REDDY COLLEGE OF ENGINEERING AND TECHNOLOGY B.Tech III Year II Semester Examinations, 2017 DIGTIAL COMMUNICATIONS (Electronics and Communication Engineering) Model Question Paper-1

Time: 3 hours

Note: This question paper contains two parts A and B.

Part A is Compulsory which carries 25 marks .Answer all questions in Part A. Part B consists of 5 Units.

Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c, as sub Questions

PART-A

(25 Marks)

Max. Marks: 75

- 1. Answer all the following questions:
 - a. What is aliasing effect? How it can be removed?
 - b. What is hunting in Delta modulation?
 - c. Draw the signal space representation for QPSK.
 - d. What are the advantages and disadvantages of DPSK when compared to PSK?
 - e. List any 2 applications of eye pattern.
 - f. What is a matched filter?
 - g. Define the terms i) Code rate ii) Code efficiency iii) Code word iv) Block length.
 - h. What is meant by systematic and non-systematic code?
 - i. What is a PN sequence?
 - j. What is Multipath Fading?

PART-B

(50 Marks)

a. State & Discuss Hartley Shannon Law.
 b. Explain the bandwidth-S/N tradeoff.

OR

- a. Discuss the advantages and disadvantages of digital communication system.
 b. State and prove sampling theorem in time domain.
- 4. a. Draw the block diagram of QPSK system (modulator & detector) and explain its working.
 - b. Derive the expression for the probability of error of QPSK.

OR

- a. Explain non- coherent ASK detector in detail.
 b. Explain detection of FSK using PLL
- 6. a. Draw the block diagram of a baseband signal receiver and explain. b. Obtain the optimum filter transfer function.

OR

7. a. State and prove properties of mutual information and entropy.

- b. Prove that H(X, Y) = H(X) + H(Y/X) = H(Y) + H(X/Y).
- 8. a. State and explain the properties of cyclic codes

b. The generator polynomial of a (7, 4) cyclic code is x^3+x+1 . Construct the generator matrix for a systematic cyclic code and find the code word for the message (1101) using the generated matrix.

OR

9. a. For the convolutional encoder shown below, draw the state diagram and the trellis diagram



b. What is a convolutional code? Compare error rates in Coded and Uncoded transmission

- 10. a. Explain how PN sequences are generated. What are the maximal length sequences? What are their properties?
 - b. Explain the Direct sequence spread spectrum (DSSS).

OR

- 11. a. Explain the ranging using direct sequence spread spectrum.
 - b. Explain the slow and fast frequency hopping techniques.

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

III B.Tech II Semester supplementary Examinations, Nov/Dec 2018

Antennas and Wave Propagation

(ECE)									
Roll No									

Time: 3 hours

Note: This question paper contains two parts A and B Part A is compulsory which carriers 25 marks and Answer all questions. Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries

10 marks.

******** PART – A

(25 Marks)

- (a) Define the terms "Beam Area" and "Radiation intensity. [2M]
 (b) Compare 'Quarter wave Monopole' and 'Half wave Dipole'. [3M]
 - (c) List out the salient features of 'Yagi –Uda Array'. [2M]
 - (d) What are the applications of 'Horn Antennas?' [3M]
 - (e) What is the use of 'Antennas arrays'? [2M]
 - (f) How Directivity measurement is done. [3M]
 - (g) Explain about Duct propagation. [2M]
 - (h) Define Critical frequency. [3M]
 - (i) What do you understand by M-Curves [2M]
 - (j) Explain about the structure of Ionosphere [3M]

PART – B

(50 Marks)

<u>SECTION – I</u>

2. Prove that the radiation resistance of half wave dipole antenna is 73 ohm . [10M]

(**OR**)

3. Discuss in detail about the Far – Fields and patterns of thin Linear centre fed antennas of different lengths. [10M]

<u>SECTION – II</u>

4. What are 'Microstrip antennas'? Discuss in detail about its features, advantages and limitations. [10M]

(OR)

5. List out all the steps involved in design considerations of pyramidal Horns. [10M]

<u>SECTION – III</u>

6. Bring out the differences between Broadside arrays and Binomial arrays. Explain in detail about binomial array including radiation pattern. [10M]

(OR)

7. Discuss in detail, about Antenna Gain measurement with respect to various methods.

[10M]

SECTION – IV

8. What is line of sight propagation? Elaborate LOS using neat labeled diagram and derive the expression for the same. [10M]

Max. Marks: 75

(OR)

9. With a neat diagram, explain in detail the principle involved in Tropospheric Propagation its advantages, limitations and applications. [10M]

SECTION - V

10. Enumerate the concept involved in "Multihop Propagation'. Also give its advantages and limitations. [10M]

(OR)

11. Discuss in detail about the Reflection of sky waves by Ionosphere virtual height and skip distance. [10M]

Code No: R15A0416

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY (Autonomous Institution – UGC, Govt. of India)

III B.Tech II Semester Regular Examinations, April/May 2018

Antennas and Wave Propagation



Time: 3 hours

Max. Marks: 75

R15

Note: This question paper contains two parts A and B

Part A is compulsory which carriers 25 marks and Answer all questions.

Part B Consists of 5 SECTIONS (One SECTION for each UNIT). Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 10 marks.

PART – A

(25 Marks)

- 1. (a) State Helmholtz theorem.[2M]
 - (b) Define the terms 'Directivity' and 'Effective Area'. [3M]
 - (c) What are the functions of "Lens Antennas" [2M]
 - (d) List of the characteristics of 'Microstrip'. [3M]
 - (e) What are the advantages of 'End fire Arrays'. [2M]
 - (f) Explain about the principle of 'Pattern Multiplication'. [3M]
 - (g) Define wave tilt of ground wave propagation . [2M]
 - (h) Compare Space and Surface waves with respect to any three parameters. [3M]
 - (i) Define the term 'Skip distance' and give its significance. [2M]
 - (j) List out the advantages of Sky wave Propagation. [3M]

PART – B

(50 Marks)

SECTION - I

2. Discuss in detail about the concepts involved in Fields from Oscillating Dipole and Field Zones. [10M]

(OR)

3. Explain in detail about Retarded Potentials and Helmholtz theorem. [10M]

SECTION - II

4. Discuss in detail about the Geometry of Non Metallic dielectric lenses and zoning. [10M]

(OR)

5. Explain about the operating principle of helical antenna in normal mode. [10M]

SECTION – III

6. Explain in detail about the features, advantages and limitations of Uniform Linear Arrays. [10M]

(OR)

7. Explain in detail about the measurement of radiation pattern with neat diagram . [10M]

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SECTION-IV

8. With respect to space wave Propagation discuss in detail about the Effect of Earths Curvature and absorption [10M]

(OR)

9. What do you understand by 'Super Refraction' Also, explain in detail about Scattering phenomena and Wave tilt. [10M]

SECTION - V

sic sic sic sic sic

- 10. With new diagrams, explain about Refraction of Sky wave by Ionosphere. [10M] (OR)
- 11. Derive the relation between MUF and Skip distance. Also, give the applications of Sky wave propagation. [10M]

(ECE)

Roll No

Time: 3 hours

Max. Marks: 75

<u>PART – A</u>

a) List the advantages of DBMS?

- b) List the database Applications?
- c) Define instances and schemas of database?

d) Discuss Data Independence?

- e) Explain database Access for applications Programs
- f) Define relational database query?

g) State about SELECT operation in Relational algebra?

h) State about PROJECT operation in Relational algebra?

i) Define Aggregate Functions?

j) Discuss the use of rename operation?

PART-B

<u>SECTION – I</u>

1. What is logical data independence and why is it important?

OR

- 2. a) What is partial key? How is it represented in ER diagram? Give an example?
 - b) What is a descriptive attribute? Explain?
 - c) Discuss the usage of ISA feature in ER diagram?

SECTION – II

3. Explain the following with examples.a) Key constraints. b) Foreign key constraints.

OR

4. What is a view? Explain about views in detail?

<u>SECTION – III</u>

5. Explain the followinga) Lossless Joinb) Lossless decomposition

OR

6. What are the advantages of normalized relations over the unnormalized relations?

SECTION - IV

7. a) How the use of 2PL would prevent interference between the two transactions.b) Explain the difference between strict 2PL and rigorous 2PL?

OR

8. Explain different recovery techniques used in transaction failure?

SECTION - V

9. Explain all the operations on B+ tree by taking a sample example

OR

10. Explain B+ Trees with examples?

(ECE)

Roll No

Time: 3 hours

Max. Marks: 75

<u>PART – A</u>

a) Define redundancy?

b) Define First Normal Form?

c) Define Second Normal Form?

d) Define Third Normal Form?

e) Define Fourth Normal Form?

f) Define a Transaction? List the properties of transaction

g) Discuss different phases of transaction?

h) Discuss recoverable schedules?

i) Discuss cascade less schedules?

j) Define Two Phase Commit protocol?

<u>PART-B</u> SECTION – I

a) Describe storage manager component of database system structure?
 b) Explain levels of abstraction in DBMS

OR

2. Explain the E-R diagram components and notations with their extended features?

SECTION – II

3. Explain the following.a) Types of Join Operationsb) Set Operations

OR

4. a) Define Relational Algebra, tuple and domain relational calculus?b) What are the differences between the two types of relational calculus?

SECTION - III

5. Define BCNF? How does BCNF differ from 3NF? Explain with an example.

OR

6. What is Redundancy? What are the different problems encountered by redundancy? Explain them.

SECTION - IV

7. What are the transaction isolation levels in SQL?

OR

8. Explain how concurrency execution of transactions improves overall system performance?

$\underline{SECTION - V}$

9. a) What is the relationship between files and Indexes?b) What is the search key for an Index?

c) What is Data entry in an Index

(ECE)

Roll No

Time: 3 hours

Max. Marks: 75

OR

10. Explain shadow-copy Technique for Atomicity and Durability.

<u>PART – A</u>

- a) Discuss about data on External storage?
- b) Explain Clustered Indexes?
- c) Discuss the Primary and Secondary indexes?
- d) Define Tree Indexing?
- e) Explain Hash based Indexing?
- f) Define (i) Database (ii) DBMS
- g) Explain about Database storage structure?
- h) Discuss Transaction management?
- i) Explain the Query Processor?
- j) Define (i) Entity (ii) Attribute

PART-B

SECTION - I

1. Define DBMS? List Database system applications.

OR

2. List four significant differences between a file processing system and a DBMS?

<u>SECTION – II</u>

3. a) Write a detail note on participation constraints?b) What is the class hierarchy? How is it represented in the ER diagrams?

OR

4. What are NULL values? Explain in detail.

SECTION - III

OR

- 5. Explain FD and MVD with examples
- **6.** What is Normalization? Discuss what are the types? Discuss the 1NF, 2NF, 3NF with example?

SECTION - IV

7. What are the types of failures of a system?

OR

8. What are the two tables used in crash recovery along with log record? Explain with suitable example?

$\underline{SECTION-V}$

9. Explain about tertiary storage media in detail?

OR

10. Explain

a) Clustered Indexes b) Primary and Secondary Indexes

(ECE)

Roll No

Time: 3 hours

Max. Marks: 75

PART-A

a) Discuss the basic form of SQL query?

b) Define Null Values.

c) Define tuple variable with its syntax?

d) Define Dynamic SQL?

e) Define Assertions?

f) List out the Problems related to decompositions?

g) Explain about Loss less-join dependency?

h) Explain about BCNF?

i) Explain about multi-valued dependencies?

j) Define join dependency and fifth normal form?

<u>PART-B</u> SECTION – I

1. Explain key constraints with an example?

OR

2. Discuss the query processor of database system structure?

<u>SECTION – II</u>

3. Explain different types of Join Operations with relevant examples.

OR

4. Explain the following in SQL with examples.
a) Nested Queries
b) Correlated Queries
c) Group by and Having Clauses
d) Triggers

SECTION - III

5. Explain about the fourth and fifth normal forms.

OR

6. Define Functional dependencies? How are primary keys related to functional dependencies?

SECTION - IV

7. Write the locking compatibility matrix used for multiple granularity? Explain with suitable examples?

OR

8. Define the concept of schedule for a set of concurrent transaction. Give a suitable example.

<u>SECTION – V</u>

9. Explain about Tree based Indexing and Hash based Indexing.

OR

10. a) Explain about fixed length file organization with an example?

b) Explain about byte-string representation in detail.

(ECE)

Roll No

Time: 3 hours

Max. Marks: 75

PART-A

a) Explain about locking protocols?

b) Define timestamp based protocol?

c) Explain about multiple granularity?

d) Explain about storage structure?

e) Explain about remote backup systems?

f) Explain about static hashing?

g) Explain about organization of records in files?

h) Discuss the impact of Workload on Indexes?

i) Explain about RAID

j) Define extendable hashing?

<u>PART-B</u> SECTION – I

1). Describe storage manager component of database system structure?

OR

2). Make a comparison between Database system and File system.

SECTION - II

3). What are the differences between the two types of relational calculus

OR

4). Define BCNF? How does BCNF differ from 3NF? Explain with an example.

SECTION - III

5). Explain strict 2PL and rigorous 2PL with example?

OR

6). Explain different recovery techniques used in transaction failure?

SECTION – IV

7). What you mean by query equivalence. Explain with example?

OR

8). Explain a) Clustered Indexes b) Primary and Secondary Indexes

<u>SECTION – V</u>

9). what is role-based access control? In what ways is it superior to DAC and MAC?

OR

10). Explain the following

- (a) What are Challenges in ORDBMS implementation?
- (b) Discuss the issues in data mining in detail.